

L&T Press Release

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L&T Semiconductor Technologies-IIT Gandhinagar Join Hands to Develop Semiconductor Solutions for Projects of National Importance

Mumbai, August 30, 2024: L&T Semiconductor Technologies (LTSC), a pioneer in indigenous semiconductor design & development, has entered a partnership with the Indian Institute of Technology, Gandhinagar (IIT Gandhinagar), for joint development of secure Integrated Circuits (IC) and System-on-Chips (SoC) projects of national importance.

The collaboration synergises LTSC's cutting-edge capabilities in semiconductor development with IIT Gandhinagar's academic excellence. Both the entities have committed to a collaborative framework encompassing research, development and training with an emphasis on fostering innovation and strengthening India's semiconductor capabilities.

A centrepiece of the collaboration is the joint development of secure ICs and SoCs for India's vital technological infrastructure such as ePassport, eDriving License, eAadhaar, Electronic Voting Machine (EVM) and NAVIC based navigation ICs.

In addition, LTSC and IIT Gandhinagar will collaborate on developing Post-Quantum Encryption Algorithms (QEA) initiative which aligns with Government of India's vision of *Aatmanirbhar Bharat* and echoes the Prime Minister's clarion call of *Jai Anushandhan*.

The partnership will also focus on capacity building and skill development, essential for bolstering India's workforce in semiconductor design and manufacturing. Through initiatives such as curriculum development, train-the-trainer programmes, hands-on workshops, and certification courses, it seeks to empower the next generation Indian techies. Congratulating both the entities,

(R&D E&IT), Ministry of Electronics and Information Technology

said: "The collaboration between L&T Semiconductor Technologies and IIT Gandhinagar is a significant step towards building a self-reliant semiconductor ecosystem in India. This partnership will focus on developing secure ICs and SoCs for critical national infrastructure, as well as exploring Post-Quantum Encryption Algorithms (QEA). The collaboration will also emphasize capacity building and skill development, essential for strengthening India's semiconductor capabilities. We look forward to the joint development of secure ICs and SoCs for India's vital technological infrastructure, such as ePassport, eDriving License, eAadhaar, Electronic Voting Machine (EVM), and NAVIC based navigation ICs. This initiative aligns with the Government of India's vision of Aatmanirbhar Bharat and echoes the Prime Minister's clarion call of Jai Anushandhan. We are confident that this partnership will empower the next generation of Indian techies and contribute to the growth of the semiconductor industry in India."

between industry and academia that is vital for realising the goals of *Aatmanirbhar Bharat*. This partnership will not only accelerate the development of indigenous technologies but also enhance our global competitiveness in the semiconductor domain."

Mr Sandeep Kumar, Chief Executive - L&T Semiconductors Technologies, said: "The partnership marks a transformative chapter in LTSCCT's journey towards driving innovation and technological advancement in India's semiconductor sector. By combining our industry-leading expertise on multiple application areas with IIT Gandhinagar's academic and innovation rigor, we are poised to address critical challenges towards 'Designed-in-India' semiconductors and contribute to the nation's vision of a self-reliant and technologically advanced country."

Speaking on the occasion, **Prof Rajat Moona, Director - IIT Gandhinagar**, said: "We are delighted to partner with LTSCCT in multiple strategic areas like niche security products, post-quantum compute-based ecosystem development and much more. This reinforces our